

SSC8066GN4

N-Channel Enhancement Mode MOSFET

> Features

V _{DS}	V _{GS}	R _{DS(ON)}	ID
60V	±20V	13mΩ@10V	36A
	<u> </u>	19mΩ@4V5	30A

> Description

This SSC8066GN4 uses advanced trench technology to provide excellent RDSON and low gate charge. The complementary MOSFETS may be used to form a level shifted high side switch, and for a host of other applications.

100% UIS + ΔVDS + Rg Tested!

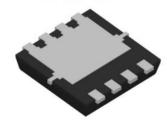
> Applications

- Load Switch
- PWM Application
- Power Management

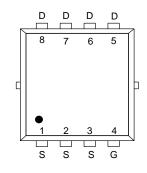
> Ordering Information

Device	Package	Shipping
SSC8066GN4	PDFN3.3X3.3-8L	5000/Reel

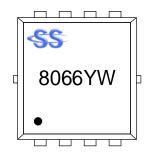
Pin configuration



PDFN3.3X3.3-8L (Bottom View)



Pin Configuration (Top View)



<u>Marking</u>

(YW: Internal Traceability Code)





Symbol	Parameter	Ratings	Unit		
V _{DSS}	Drain-to-Source Voltage		60	V	
V _{GSS}	Gate-to-Source Voltage		±20	V	
	Continuous Drain Current ^d	Tc=25℃	36	^	
ID		Tc=100℃	19	A	
Idsm	Continuous Drain Current ^a	T _A =25℃	12		
		T _A =70℃	8.8	A	
Ідм	Pulsed Drain Current ^b		142	А	
D	Power Dissipation ^c	Tc=25℃	27	14/	
PD		Tc=100℃	11	W	
Pdsm	Power Dissipation ^a	T _A =25℃	2.8	W	
		T _A =70℃	1.8		
las	Avalanche Current ^b L=0.5mH Single Pulse		16	А	
Eas	Avalanche Energy ^b L=0.5mH Single Pulse		64	mJ	
TJ	Operation junction temperature		-55~150	°C	
Tstg	Storage temperature range		-55~150	°C	

> Absolute Maximum Ratings ($T_A=25^{\circ}$ unless otherwise noted)

> Thermal Resistance Ratings ($T_A=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
R _{0JA}	Junction-to-Ambient Thermal Resistance ^a	35	°C/W
R _{θJC}	Junction-to-Case Thermal Resistance	4.4	C/W

Note:

- a. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with T_A=25 °C.The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.



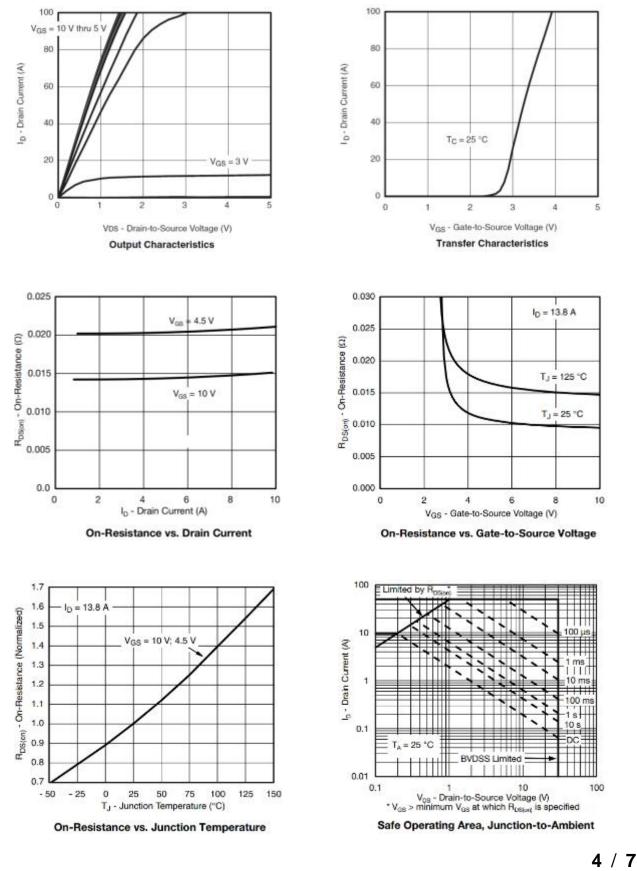


> Electrical Characteristics (T_A=25 $^{\circ}$ C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = 250 \mu A$	60			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 uA$	1.4	1.9	2.5	V
Drain-Source On-Resistance	R _{DS(on)}	$V_{GS} = 10V, I_D = 9A$		13	20	
		$V_{GS} = 4.5V, I_D = 6A$		19	29	-mΩ
Zero Gate Voltage Drain Current	Idss	$V_{\text{DS}} = 60 \text{V}, V_{\text{GS}} = 0 \text{V}$			1	μA
Gate-Source Leak Current	lgss	$V_{GS} = \pm 20V$, $V_{DS} = 0V$			±100	nA
Transconductance	G _{FS}	$V_{DS} = 5V, I_D = 10A$		16		s
Forward Voltage	Vsd	$V_{GS} = 0V$, $I_S = 9A$			1.4	V
Gate Resistance	Rg	$V_{DS} = 0V, f = 1MHz$		1.4	2.2	Ω
Input Capacitance	Ciss			1070		pF
Output Capacitance	Coss	$V_{DS} = 15V, V_{GS} = 0V,$		108		
Reverse Transfer Capacitance	Crss	f = 1MHz		86		
Total Gate Charge	Q _G	V 40V/V 20V		18		
Gate to Source Charge	Q _{GS}	$V_{GS} = 10V, V_{DS} = 30V,$ $I_{D} = 15A$		9		nC
Gate to Drain Charge	Q _{GD}	10 = 15A		6		
Turn-on Delay Time	T _{D(ON)}			9		
Rise Time	Tr	$V_{GS} = 10V, V_{DS} = 10V, R_L$		4		
Turn-off Delay Time	T _{D(OFF)}	= 10Ω , $R_G = 1\Omega$		15		ns
Fall Time	T _f			6		
Diode Recovery Time	Trr	I _F =20A, di/dt=500A/us		12		ns
Diode Recovery Charge	Qrr	I _F =20A, di/dt=500A/us		19		nC



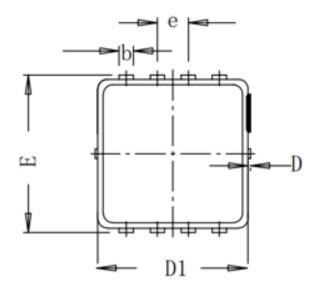
> Typical Performance Characteristics (T_A=25 $^{\circ}$ C unless otherwise noted)

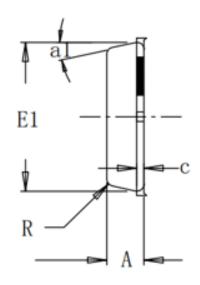


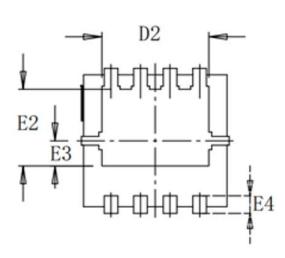


SSC8066GN4

> Package Information







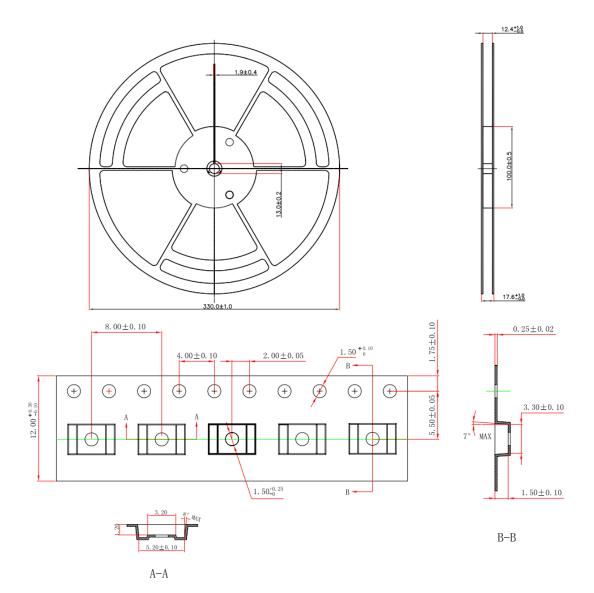
Dim	Millimeters			
Dim	Min.	Nom.	Max.	
Α	0.75	0.78	0.81	
b	0.297	0.3	0.35	
С	-	0.152	-	
D	0	0.05	0.1	
D1	3.12	3.15	3.18	
D2	-	2.35	-	
Е	3.2	3.3	3.4	
E1	3.09	3.12	3.15	
E2	-	1.75	-	
E3	-	0.575	-	
E4	-	0.4	-	
R	-	0.15	-	
e	0.65BSC			
a1°	-	12°	-	







> Tape and Reel





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